IN THE SPECIFICATION:

Please amend paragraph number 0143 as follows:

The foregoing capabilities can be used by system designers to define an appropriate write offset value. Many different algorithms may be implemented as a matter of routine design choice which result in a "centering" write offset value using a minimum amount of time and resources. For example, a simple single data transition might be written from master 11 to slave 12a in a large block of data, say 128 bits consisting of 64 ones followed by 64 zeros. These bits are stored in the slave and read back to the master. The master determines where in the data block a one-to-zero transition occurred, and uses this information to increment or decrement the ~~read~~ write offset value. This sequence of steps continues until the offset value dithers back and forth between "0" and "1" (i.e., "toggles") with each iteration at which point a centering offset value has been obtained.